# **Parallelism in LP and MIP**

Thank you for joining us. We will be starting shortly.



The World's Fastest Solver

#### **Today's Speaker**





#### Ed Rothberg

CEO, Co-Founder

**Gurobi Optimization** 

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# How to Exploit Parallelism in Linear Programming and Mixed-Integer Programming

Ed Rothberg, CEO & Co-founder, Gurobi Optimization



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## Outline



- Parallelism in LP
- Parallelism in MIP
- Distributed algorithms
  - Distributed MIP
  - Distributed tuning
  - Distributed concurrent

#### Other metrics

- Maximizing throughput on a parallel server
- Other architectures
  - Graphical Processing Unit (GPU)
  - Quantum computer



## **Important Concepts**

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#### **Sources of Parallelism**



#### • We exploit 3 sources of parallelism in Gurobi

- Parallel algorithms
  - Divide up 'fixed' pile of work
- Diversity of algorithms
  - Given a mix of algorithms...
    - Run them all at once
    - Stop when the first one finishes
- Performance variability
  - Given an algorithm that can experience large performance swings on the same problem...
    - Run multiple instances at once with multiple settings
    - Stop when the first one finishes

### Non-determinism



- Parallel algorithms often exhibit non-deterministic behavior
  - Same problem, same machine -> different (equivalent) results
- Nearly all Gurobi parallel algorithms are deterministic
  - Same problem, same machine -> same result every time
- A few Gurobi parallel algorithms exhibit mild non-determinism
  - A small number of possible outcomes
  - Examples:
    - Concurrent LP
    - Concurrent MIP
- We avoid highly non-deterministic algorithms

### **Determinism – How?**



- Basic principle for avoiding non-determinism...
  - Which thread finished first?
  - Answer must be the same every time
- Options:
  - Wall-clock time: not reliable
  - CPU counters: not exposed, few are reliable
  - Instrument the code
- Every algorithm in Gurobi makes an estimate of how much work it did
  - Which thread finished first?
  - The one with the smaller work estimate



# **Parallelism in LP**

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#### Two Fundamental Algorithms for Linear Programming



- Simplex algorithm (primal and dual)
- Interior point (barrier) algorithm
- When considering parallelism in LP, more like...
  - One workhorse (simplex)
  - Plus one very sophisticated initial basis selection procedure (barrier)

### Simplex Algorithm





- Iterate until no more improving direction is found
  - This is an optimal solution to the LP.

## Simplex Algorithm – Linear Algebra



Primal feasibility constraints

Ax = b

- Partition into basic and non-basic variables: A = (B, N)
  - Non-basic structural variables correspond to tight bounds
  - Non-basic slack variables correspond to tight constraints

 $Bx_B + Nx_N = b$ 

• Solve for basic variables

$$x_B = B^{-1}(b - Nx_N)$$

Solved by maintaining

B = LU

- Perform a sequence of pivots
  - Swap one non-basic variable for one basic variable
  - Update basis matrix (and basis factor)



#### Simplex Log



Iteration	Objective	Primal Inf.	Dual Inf.	Time
0	1.7748600e+04	6.627132e+03	0.000000e+00	0s
9643	1.1574611e+07	1.418653e+03	0.000000e+00	5s
14440	1.1607748e+07	4.793500e+00	0.000000e+00	10s
15213	1.1266396e+07	0.000000e+00	0.000000e+00	11s

Solved in 15213 iterations and 10.86 seconds Optimal objective 1.126639605e+07

#### **Interior Point Method**





• Jump to the analytic center of the optimal face

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#### **Interior Point Method**



- Basic algorithm [Dikin, 1967, Karmarkar, 1984, Fiacco & McCormick, 1990]:
  - Modify KKT conditions:

• Linearize complementarity condition:

 $\begin{pmatrix} -\theta & A^T \\ A & 0 \end{pmatrix} \begin{pmatrix} dx \\ dy \end{pmatrix} = \begin{pmatrix} r_2 \\ r_1 \end{pmatrix}$  (augmented system)

 $\theta_j = z_j/x_j, \ x_j \cdot z_j = 0$  at optimality, so  $\theta_j \to 0$  or  $\infty$ 

- Further simplification:  $A\theta^{-1}A^T dy = b$  (normal equations)
- Iterate, reducing  $\mu$  in each iteration
- Provable convergence

### **Interior Point Computational Steps**



- Setup steps:
  - Presolve (same for simplex)
  - Compute fill-reducing ordering
  - Symbolic factorization allocate static data structures for factor
- In each iteration:
  - Form  $A\theta^{-1}A^T$
  - Factor  $A\theta^{-1}A^T = LDL^T$  (Cholesky factorization)
  - Solve  $LDL^T x = b$
  - A few Ax and  $A^Tx$  computations
  - A bunch of vector operations
- Post-processing steps:
  - Perform crossover to a basic solution
    - Optional, but typical
    - Especially when solving LP relaxations in a MIP solve

#### **Barrier Log**



Barrier statistics: AA' NZ : 2.836e+03 Factor NZ : 3.551e+03 (roughly 40 MBytes of memory) Factor Ops : 1.739e+05 (less than 1 second per iteration) Threads : 4

	Obje	ective	Resid	dual		
Iter	Primal	Dual	Primal	Dual	Compl	Time
0	1.30273209e+06	0.0000000e+00	5.90e+02	0.00e+00	7.32e+00	12s
1	1.04326180e+05	-5.84079103e+02	4.84e+01	1.69e+00	5.95e-01	12s
2	9.46325157e+03	-4.40392705e+02	2.92e+00	1.35e+00	5.46e-02	12s
3	3.66683689e+03	9.27381244e+02	1.94e-01	5.35e-01	1.41e-02	12s
4	3.37449982e+03	1.79938013e+03	1.29e-01	2.41e-01	7.64e-03	12s
5	3.13244138e+03	1.90266941e+03	8.89e-02	2.07e-01	6.00e-03	12s
6	2.71282610e+03	2.11401255e+03	3.20e-02	1.15e-01	2.96e-03	12s
7	2.48856811e+03	2.18107490e+03	1.06e-02	7.26e-02	1.56e-03	12s
8	2.35427593e+03	2.21183615e+03	3.20e-03	4.52e-02	7.36e-04	12s
9	2.30239737e+03	2.22464753e+03	1.53e-03	2.38e-02	4.03e-04	12s
10	2.25547118e+03	2.23096162e+03	3.00e-04	1.40e-02	1.30e-04	12s
11	2.24052450e+03	2.23917612e+03	4.10e-06	6.33e-04	7.20e-06	12s
12	2.23967243e+03	2.23966346e+03	2.01e-08	5.01e-06	4.82e-08	12s
13	2.23966667e+03	2.23966666e+03	1.11e-10	1.14e-13	4.81e-11	13s

Barrier solved model in 13 iterations and 12.51 seconds Optimal objective 2.23966667e+03

#### **Crossover Log**



```
Barrier solved model in 13 iterations and 12.51 seconds
Optimal objective 2.23966667e+03
Root crossover log...
40 DPushes remaining with DInf 0.0000000e+00
13s
0 DPushes remaining with DInf 7.8159701e-14
13s
1176 PPushes remaining with PInf 0.0000000e+00
0 PPushes remaining with PInf 0.000000e+00
13s
Push phase complete: Pinf 0.0000000e+00, Dinf 1.2079227e-13
13s
```

Root simplex log...

Iteration	Objective	Primal Inf.	Dual Inf.	Time
1219	2.2396667e+03	0.000000e+00	0.000000e+00	13s
1219	2.2396667e+03	0.000000e+00	0.000000e+00	13s
Root relax	ation: objective	2.239667e+03,	1219 iterations,	0.43 seconds

### **Essential Differences for Parallelism**



#### • Simplex:

- Thousand/millions of iterations on extremely sparse matrices
- Each iteration extremely cheap
- Very limited opportunities to exploit parallel

#### • Barrier:

- Dozens of expensive iterations
- Much denser matrices
- Lots of opportunities to exploit parallelism
  - But...

#### **Concurrent LP**



#### • Run both simplex and barrier simultaneously

- Thread 1: Dual simplex
- Thread 2: Barrier
- Thread 3: Barrier
- Thread 4: Barrier
- Thread 5: Primal simplex
- Thread n≥6: Barrier
- Solution is reported by first one to finish
- Use multiple CPU cores to exploit a diverse set of algorithms
- Best mix of speed and robustness
- Deterministic and non-deterministic versions available

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#### 21

# LP Performance

#### • Performance results:

- Simplex on 1 core, barrier on all available cores
- Concurrent:
  - 4 cores: 1 thread dual, 3 threads barrier
  - 16 cores: 1 thread primal, 1 thread dual, 14 threads barrier
- Models that take >1s

#### 4-core Xeon E3-1240

	GeoMean
Primal simplex	3.65
Dual simplex	2.25
Barrier	1.20
Concurrent	1.00
Deterministic Concurrent	1.13

#### 16-core EPYC 7282

	GeoMean
Primal simplex	3.73
Dual simplex	2.56
Barrier	1.27
Concurrent	1.00
Deterministic Concurrent	1.20





# **Parallel Barrier Performance**

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#### **Parallel Barrier Performance**



- Speedups from adding cores
  - On our internal LP test set
    - 1299 models
  - AMD EPYC 7282 (16 cores, 2.8GHz base, 3.2GHz boost, 64GB 2933MHz DDR4)
  - Relative to one core

	# models	P=1	P=2	P=4	P=8	P=16
>1s	602	1.00	1.28	1.54	1.76	1.92
>10s	430	1.00	1.34	1.69	1.97	2.20
>100s	249	1.00	1.46	1.86	2.22	2.56

#### **Barrier Runtime Breakdown**







#### **Barrier Runtime Breakdown**









# **Parallelism in MIP**

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#### MIP Solution Framework: LP-based Branch-and-Bound





### **Parallel MIP = Parallel Branch and Bound**



• MIP explores a tree of relaxations



• Frontier nodes are independent and can be explored in parallel

#### **Parallel MIP Performance**



- Speedups from adding cores
  - On our internal MIP test set
    - 3965 models
  - AMD EPYC 7282 (16 cores, 2.8GHz base, 3.2GHz boost, 64GB 2933MHz DDR4)
  - Relative to one core

	# models	P=1	P=2	P=4	P=8	P=16
>1s	2654	1.00	1.26	1.72	1.96	2.11
>10s	1907	1.00	1.32	1.91	2.24	2.47
>100s	1087	1.00	1.42	2.21	2.74	3.08
>1000s	319	1.00	1.68	3.03	3.86	4.22

#### **Parallel MIP Performance Versus Optimality Gap**



• Parallel speedup versus optimality gap (>100s – 1087 models)



Tests run 2020-07-20

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## What Limits MIP Parallelism?



#### • Tree shape

- Fraction of time spent at the root
- Total number of nodes explored
- Load balancing
- Topology of the tree

## What Limits MIP Parallelism?



- Tree shape
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## What Happens at a Node?

- Multiple steps at each node
  - Node presolve
  - LP relaxation solve
  - Cutting planes
  - Heuristics
  - Branch variable selection



#### What Happens at the Root?



- Root node repeats these steps many times
  - 10+ passes not unusual
- Vital to make as much progress as possible before branching



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#### Parallelism at the Root

- Options for exploiting multiple cores at the root?
- Exploit performance variability [Fischetti, Lodi, Monaci, Salvagnin, 2014]
  - Start one or more *helper* threads
  - Same steps as main thread, but perturbed slightly
  - Feed results back to main thread
    - Heuristic solutions
    - Cutting planes
- Limited benefit



## What Limits MIP Parallelism?



- Tree shape
  - Fraction of time spent at the root
  - Total number of nodes explored
  - Load balancing
  - Topology of the tree



### **Parallel MIP Performance (By Node Count)**



• Geometric mean speedup on 16 cores



## What Limits MIP Parallelism?



- Tree shape
  - Fraction of time spent at the root
  - Total number of nodes explored
  - Load balancing
  - Topology of the tree



## What Limits MIP Parallelism?

- Tree shape
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  - Topology of the tree





# **Other Options**

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#### **Concurrent MIP**



- **Use** ConcurrentMIP=n **parameter** 
  - n independent MIP jobs
    - Divide available threads among requested jobs
      - Example: 24 threads available, ConcurrentMIP=4: 4 jobs, 6 threads each
    - Results combined automatically
    - Settings
      - Default: different random seeds
      - User can control with concurrent environments
  - Non-deterministic

#### • Scope for improvement

- Exploit unpredictability
  - Best (random) result wins
- Focus on different goals simultaneously
  - E.g., one run works on lower bound (cuts, etc.), one works on upper bound (heuristics, etc.)

#### • Not as effective as you might hope in general

• Some notable exceptions

#### **Concurrent MIP – How To**



Work

230

195

193

192

192

191

162

162

162

162

163

162

155

144

143

146

148

It/Node Time

2s

3s

3s

5s

5s

5s

5s

9s

9s

9s

9s

9s

9s

10s

17s

20s

29s

30s

Gap |

97.9%

97.6%

96.9%

96.98

96.5%

95.4%

94.9%

85.3%

84.6%

81.7%

79.8%

78.6%

78.3%

78.3%

78.3%

78.3%

78.2%

78.2%

Default settings

16 340 3575756.50 780507.097

- Simplest approach: use ConcurrentSettings command-line parameter
  - MIPFocus1.prm: MIPFocus 1 (focus on feasible solutions)
  - MIPFocus3.prm: MIPFocus 3 (focus on lower bound)
- Result for model dg012142...

#### ConcurrentSettings=MipFocus1.prm,MipFocus3.prm

Nodes	1	Curre	nt Node	<u>م</u> ا	Objective	Bounds	I	Work		N	odes	I	Current N	ode		Objecti	ve Bounds
Expl Ur	nexpl	Obj	Depth	IntInf	Incumbent	BestBd	Gap	It/Nod	e Time	E 	xpl U	nexpl	Obj Dep	th In	tInf	Incumben	t BestBd
83 149 180 188 196	2 92 156 187 195 233	-			6492675.78 7 5148647.37 7 3044594.17 7 2984859.29 7 2727098.75 8 2720275.25 8	78732.766 85662.888 85662.888 36163.745 59256.557	88.0% 84.7% 74.2% 73.7% 69.3% 68.4%	-	8s 10s 19s 22s 26s 36s	н н н н н н н н	0 126 365 623 625 635 649 1309 1312 1316 1320 1322 1346 1530 2466 2483	2 129 374 651 651 651 1146 1144 1123 1107 1101 1212 1926 1937	772593.572 1484472.12 782874.062 1301398.44 3018571.09	0 29 6 29 72	410 3. 2. 330 2. 1. 1. 52 50 42 38 36 35 367 410 425	3.7336e+07 181386e+07 537530e+07 2.5375e+07 228999e+07 700295e+07 524965e+07 65663.3333 32846.8222 28610.0476 36260.5089 28016.2000 75756.5000 3575756.50 3575756.50 3575756.50	772593.572 775156.902 775156.902 775156.902 775156.902 775156.902 775156.902 775156.902 775156.902 775156.902 775156.902 775156.902 775156.902 775156.902 775636.708 775636.708
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1983 2621748.16



# **Distributed Algorithms**

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## **Distributed Architecture**

- Multiple, ideally identical machines connected by a network
  - On premise
  - Gurobi Instant Cloud
- Manager-worker paradigm
  - Manager distributes work among workers
  - Workers perform work and report results
  - Manager collects results





## **Distributed MIP**



- **Use** DistributedMIPJobs=n **parameter**
- Actually a combination of concurrent and parallel tree exploration
  - Ramp up: concurrent for a limited number of nodes [ParaSCIP, 2010]
  - Parallel tree exploration: continue with the 'best' concurrent result
- Dramatically higher node throughput...
  - ...when the search tree makes lots of independent nodes available

Model	1 Machine	16 Machines	32 Machines
danoint	1933s	196s	128s
	912K nodes	<mark>9.9X</mark> faster	15.1X faster

• Easy to try using Gurobi Instant Cloud

Gurobi Version 9.0.3 Intel Xeon E3-1240 v3 CPUs

## **Distributed Tuning**

- Automatic parameter tuning
- Use TuneJobs=n parameter for distributed parallel tuning
- Trivially parallel
  - Explore different parameter settings in parallel
- Typical to get linear parallel speedups
  - With 24 machines, a day becomes an hour



# **Other Metrics**

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## **Thread Control on a Shared Machine**



- Imagine multiple optimization jobs share a single machine
- How many threads should each one use?
  - Too few: leave cores idle
  - Too many: multiple jobs fight over cores

### **Thread Control on a Shared Machine**



- Instead of measuring completion time for one model, measure machine throughput
  - Number of times model danoint can be solved in our hour
  - Running 1, 2, 4, 8, 16 or 16 jobs simultaneously, using different per-job core counts
  - 24-core Intel Xeon Gold 5118, 2.3GHz, 512GB DDR3 system

Threads per job	1 Job	2 Jobs	4 Jobs	8 Jobs	16 Jobs
12	15.0	27.7	38.9	38.4	38.0
19				38.0	38.4
24	27.7	40.7	40.0	39.9	38.9

• No need to worry about matching thread count to core count

Tests run 2020-08-08



# **Other Architectures**

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## **Graphical Processing Unit (GPU) Computing**



• Single-Instruction Multiple-Data (SIMD) Computing



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#### **Quantum Computing**



The Quantum Computing Company <sup>w</sup>	COMPANY 🗸	LEAP 🔻	TECHNOLOGY 🗸	APPLICATIONS -	● RESOURCES <del>▼</del>	CAREERS NEWS ▼	CONTACT	Q		
Working on a response to COVID-19? Get free, immediate access to both the Leap quantum cloud service and assistance from a community of quantum experts.										

2000 User-developed early quantum applications on D-Wave systems, including airline scheduling, election modeling, quantum chemistry simulation, automotive design, preventative healthcare, logistics, and much more.

#### Optimization



#### **Machine Learning**



#### **Materials Science**



Learn More

#### **Quantum Computing**



- Interesting future technology
- Potential to substantially speed up optimization tasks
- Currently still a science project

#### **Conclusions**



- Parallelism used throughout the Gurobi Optimizer
  - LP (barrier and concurrent)
  - MIP
  - Distributed MIP
  - Distributed tuning
- Significant performance improvements in most cases
  - Not linear
  - Problem dependent
- Continued focus area
  - Parallelism continues to become more important

## **Thank You – Questions?**



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#### **Your Next Steps**



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- A recording of this webinar, including the slides, will be available in roughly one week.